

CLAIM AMENDMENTS

Claims 1-50 (canceled).

Claim 51 (new): A computing system for securely accessing two separate networks, comprising:

a central processing unit which is able to operate in a first and a second operation status;

a first storage unit storing information of said first operation status wherein said information is saved when said computing system is switched from said first operation status to said second operation status and is utilized by said central processing unit to resume computing operation in said first operation status when said computing system is switched from said second operation status to said first operation status, wherein said first storage unit further comprises a first memory storing program addresses or data information of said first operation status; a first display memory storing display information of said first operation status; and a first hard disk;

a second storage unit storing information of said second operation status wherein said information is saved when said computing system is switched from said second operation status to said first operation status and is utilized by said central processing unit to resume computing operation in said second operation status when said computing system is switched from said second operation status to said first operation status, wherein said second storage unit further comprises a second memory storing program addresses or data information of said second operation status; a second display memory storing the display information of said second operation status; and a second hard disk;

a first network which is communicatively connected with said system in said first operation status only; and

a second network which is communicatively connected with said system in said second operation status only, wherein said first and second networks are physically separated, wherein said first network can not be connected in said second operation status, and said second network can not be connected in said first operation status;

anda switch device operatively communicated with said central processing unit to switch said operation status between said first and second operation status, wherein in said first operation status, said central processing unit is connected to said first storage unit, and said computing system is connected with said first network, wherein in said second operation status, said central processing unit is connected to said second storage unit, and said computing system is connected with said second network, wherein during the switching of operation status said central processing unit doesn't execute other computing operation.

Claim 52 (new): The computing system, as recited in claim 51, wherein said switch device comprises a status switch command input unit adapted to trigger an input signal for switching between said first operation status and said second operation status; a secured switch control unit communicating with said command input unit and said central processing unit in such a manner that when said input signal is triggered, said secured switch control unit is adapted to generate a switch control signal which is then transmitted to said central processing unit for initialing a switching task between said first operation status and said second operation status; and a connection switch unit communicating with said secured switch control unit for switching connection between said first and second storage unit and said first and second network when said central processing unit is ready.

Claim 53 (new): The computing system, as recited in claim 52, wherein during said switching process, said secured switch control unit prevents said central processing unit from performing other program and reacting to other interrupts.

Claim 54 (new): The computing system, as recited in claim 53, wherein said secured switch control unit sends a non-maskable interrupt (NMI) to said central processing unit to execute operation status switching when said input signal is triggered.

Claim 55 (new): The computing system, as recited in claim 52, wherein said secured switch control unit further comprises an identification verification unit which is adapted to verify an identify of a user giving said external switching command so as to ensure said switching between said first operation status and said second operation status is carried out by an authorized and legitimate user.

Claim 56 (new): The computing system, as recited in claim 54, wherein said secured switch control unit further comprises an identification verification unit which is adapted to verify an identify of a user giving said external switching command so as to ensure said switching between said first operation status and said second operation status is carried out by an authorized and legitimate user.

Claim 57 (new): The computing system, as recited in claim 51, wherein said information of said first operation status which is save in said first storage unit when said computing system is switched from said first operation status to said second operation status comprises data in the alterable status register in said first operation status, wherein said information of said second operation status which is save in said second storage unit when said computing system is switched from said second operation status to said first operation status comprises data in the alterable status register in said second operation status.

Claim 58 (new): The computing system, as recited in claim 57, wherein said data in said alterable status register saved in said first storage unit is utilized to resume said alterable status register when said computing system is switched from said second operating status to said first operation status, wherein said data in said alterable status register saved in said second storage unit is utilized to resume said alterable status register when said computing system is switched from said first operating status to said second operation status.

Claim 59 (new): The computing system, as recited in claim 52, wherein said secured switch control unit comprises a write-protect memory communicating with said central processing unit, wherein said write-protect memory stores the control commands for said central processing unit to perform operation status switching; and a monitoring unit adapted to receive said input signal for switching operation status from said command input unit, and interrupt said central processing unit to perform said control commands for operation status switching.

Claim 60 (new): The computing system, as recited in claim 54, wherein said secured switch control unit comprises a write-protect memory communicating with said central processing unit, wherein said write-protect memory stores the control commands for said central processing unit to perform operation status switching; a monitoring unit adapted to receive said input signal for switching operation status from said command

input unit, and interrupt said central processing unit to perform said control commands for operation status switching.

Claim 61 (new): The computing system, as recited in claim 56, wherein said secured switch control unit comprises a write-protect memory communicating with said central processing unit, wherein said write-protect memory stores the control commands for said central processing unit to perform operation status switching; a monitoring unit adapted to receive said input signal for switching operation status from said command input unit, and interrupt said central processing unit to perform said control commands for operation status switching.

Claim 62 (new): The computing system, as recited in claim 58, wherein said secured switch control unit comprises a write-protect memory communicating with said central processing unit, wherein said write-protect memory stores the control commands for said central processing unit to perform operation status switching; a monitoring unit adapted to receive said input signal for switching operation status from said command input unit, and interrupt said central processing unit to perform said control commands for operation status switching.

Claim 63 (new): The computing system, as recited in claim 59, further comprises a set trigger electrically connected to said monitoring unit for sending out a NMI signal to said central processing unit when switching operation status is allowed, and a reset trigger electrically connected to said monitoring unit and said set trigger in such a manner that when switching of said two operation statuses is finished, said monitoring unit is adapted to send a signal to said reset trigger, which then reset said set trigger and mask said switching function of said connection switch unit for preventing illegitimate switching between said two statuses.

Claim 64 (new): The computing system, as recited in claim 60, further comprises a set trigger electrically connected to said monitoring unit for sending out a NMI signal to said central processing unit when switching operation status is allowed, and a reset trigger electrically connected to said monitoring unit and said set trigger in such a manner that when switching of said two operation statuses is finished, said monitoring unit is adapted to send a signal to said reset trigger, which then reset said set trigger and mask said switching function of said connection switch unit for preventing illegitimate switching between said two statuses.

Claim 65 (new): The computing system, as recited in claim 61, further comprises a set trigger electrically connected to said monitoring unit for sending out a NMI signal to said central processing unit when switching operation status is allowed, and a reset trigger electrically connected to said monitoring unit and said set trigger in such a manner that when switching of said two operation statuses is finished, said monitoring unit is adapted to send a signal to said reset trigger, which then reset said set trigger and mask said switching function of said connection switch unit for preventing illegitimate switching between said two statuses.

Claim 66 (new): The computing system, as recited in claim 62, further comprises a set trigger electrically connected to said monitoring unit for sending out a NMI signal to said central processing unit when switching operation status is allowed, and a reset trigger electrically connected to said monitoring unit and said set trigger in such a manner that when switching of said two operation statuses is finished, said monitoring unit is adapted to send a signal to said reset trigger, which then reset said set trigger and mask said switching function of said connection switch unit for preventing illegitimate switching between said two statuses.

Claim 67 (new): A method of securely switching at least two operation statuses to access at least two physically separated networks alternatively, wherein said method comprises the steps of:

- (a) receiving a request for switching said computing system from a first operation status to a second operation status by a command input unit;
- (b) analyzing said request for switching to determine whether to execute;
- (c) sending a non-maskable interrupt to said central processing unit to process operation status switching if said request switching is accepted;
- (d) saving information of said first operation status in a first storage unit wherein said first storage unit is not accessible in said second operation status;
- (e) connecting to a second storage unit wherein said second storage unit is not accessible in said first operation status;

- (f) connecting to a second network;
- (g) reading information of said second operation status;
- (h) processing requested function in said second operation status;
- (i) saving information of said second operation status;
- (j) connecting to said first storage unit;
- (k) connecting to said first network;
- (l) reading information of said first operation status; and
- (m) switching back to said first operation status.

Claim 68 (new): The method, as recited in claim 47, wherein in step (b) further comprises a step of verifying an identification of a user executing said request for switching said computing system from said first operation status to said second operation status.

Claim 69 (new): The method, as recited in claim 48, further comprises a step of masking a switching function during said switching of operation status, so as to minimize a possibility of said operation status being switched illegitimately.

Claim 70 (new): The method, as recited in claim 49, further comprises a step of ensuring that said processing operation status switching is executed only by a prearranged control program so as to prohibit illegitimate switching of said operation status.